

MARTe2-components - Design #1205

NI9157 Introduce Clear FPGA feature

22.08.2022 15:52 - Pedro Lourenco

Status:	Closed	Start date:	22.08.2022
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Description			
Add the <i>NiFpga_ClearFpga()</i> method in the NI9157Device (MARTe2-Components/Interface) to ensure that nothing is running before a new firmware is loaded and /or a new application is started.			

History

#1 - 22.08.2022 15:52 - Pedro Lourenco

- Status changed from New to Arch: Rev

#2 - 22.08.2022 16:07 - Pedro Lourenco

1. Added the target feature, enabled using optional parameter "Clear = 1" at the NI9157Device Interface.
2. It is currently used at the AmpegonPsTb project.
3. Added two dedicated GTests to preserve component line coverage.
4. Re-ran all NI9157 related components GTests (DataSources & Interfaces) using CCS 7.0 at AMD EPYC CPU:

- At FTSrv1 w/ RIO1/NI9159
- CreateNI9157DeviceOperatorGTest. **OK 12/12**
- CreateNI9157DeviceOperatorIGTest. **OK 4/4**
- NI9157DeviceGTest. **OK 53/53**
- NI9157DeviceOperatorDatabaseGTest. **OK 20/20**
- NI9157DeviceOperatorTGTest. **OK 89/89**
- NI9157DeviceOperatorTIGTest. **OK 11/11**
- NI9157MemoryOperationsHelperGTest. **OK 2/2**
- NI9157CircularFifoReaderGTest. **OK 40/40**
- NI9157MxiDataSourceGTest. **OK 18/18**
- SampleCheckerGTest. **OK 6/6**
- CounterCheckerGTest. **OK 19/19**
- MarkerBitCheckerGTest. **OK 13/13**

See branch [[https://vcis-gitlab.f4e.europa.eu/aneto/MARTe2-components/-/tree/NI9157_ClearFpga]]

#3 - 31.08.2022 09:08 - André Neto

- Status changed from Arch: Rev to Unit: Rev

#4 - 31.08.2022 09:08 - André Neto

- Assignee changed from Pedro Lourenco to André Neto

#5 - 02.09.2022 08:48 - André Neto

- Assignee deleted (André Neto)

- Status changed from Unit: Rev to Closed

Merged with develop in e62b94074c2e9fe31f8747fa3b949f8eb0b0f764

Small lint corrections in a75fc5c14c3ca5a4ee9ba0678784d09c482234d9