

MARTe2-core - User story #155

Lint: ProcessorType

28.06.2015 19:02 - André Neto

Status: Closed	Start date: 28.06.2015
Priority: Normal	Due date:
Assignee:	% Done: 0%
Category:	Estimated time: 0.00 hour
Target version: 0.2	Spent time: 0.00 hour
Git branch (link):	SVN commit (link/?p=rev):
Git merge to develop (link):	

Description

Verify and implement coding standard using Flexelint.

Source code files modified

Architecture & design review

Date of the review: 24/07/2015

Person who did the review: Giuseppe Ferrò

Version of architecture & design document: N/A. As per the original MARTe implementation, this is a support function and does not require formal design in UML.

Result of review: N/A

List of non-conformities: N/A

Code and documentation review

Date of the review: 24/07/2015

Person who did the review: Giuseppe Ferrò

Result of review: PASS (MISRA compliant)

List of non-conformities: N/A.

Unit test review

Date of the review: 24/07/2015

Person who did the review: Giuseppe Ferrò

Result of coverage tests review: PASS

Result of functional tests review: PASS

Result of review: PASS

List of non-conformities:

Associated revisions

Revision b9e5b8d0 - 03.07.2015 17:23 - André Neto

Linting of ProcessorType #155.

Revision f56b1e85 - 03.07.2015 17:27 - André Neto

Use ProcessorType::GetProcessMask #155.

Corrected to use the getter method instead of accessing directly to the variable.

Revision ba9a2709 - 03.07.2015 17:28 - André Neto

Use ProcessorType::GetProcessorMask #155.

Corrected test to use the new ProcessorType getter method.

Revision dc12adbb - 03.07.2015 21:01 - André Neto

Corrected ProcessorTypeTest to use GetProcessorMask() #155.

Revision 7235bc67 - 06.07.2015 19:49 - André Neto

Finished linting of ProcessorType #155.

Linting of the ProcessorType class finished.

Revision 1ebcb815 - 09.07.2015 12:44 - André Neto

Linted implementation of ProcessorType completed #155.

The ProcessorType class was implemented and linted. The tests executed with no errors and all the lint issues were addressed as comments in the code.

Revision 075b5d4c - 24.07.2015 18:57 - Giuseppe Ferro

ProcessorType unit tests done.
#155

History

#1 - 02.07.2015 17:20 - Riccardo Vitelli

- Target version set to Backlog

#2 - 02.07.2015 18:07 - Riccardo Vitelli

- Target version changed from Backlog to 0.2

#3 - 02.07.2015 18:08 - Riccardo Vitelli

- Status changed from New to Code: Impl

#4 - 03.07.2015 17:12 - André Neto

- Assignee set to André Neto

#5 - 09.07.2015 14:21 - Riccardo Vitelli

- Status changed from Code: Impl to Code: Rev

#6 - 09.07.2015 14:22 - Riccardo Vitelli

- Assignee deleted (André Neto)

#7 - 24.07.2015 18:13 - Giuseppe Ferro

- Assignee set to Giuseppe Ferro

#8 - 24.07.2015 18:16 - Giuseppe Ferro

- Description updated

Code Review done: 46a0204

#9 - 24.07.2015 18:16 - Giuseppe Ferro

- Status changed from Code: Rev to Unit: Impl

#10 - 24.07.2015 18:18 - Giuseppe Ferro

- Assignee deleted (Giuseppe Ferro)

#11 - 24.07.2015 18:52 - Giuseppe Ferro

- Assignee set to Giuseppe Ferro

#12 - 24.07.2015 19:00 - Giuseppe Ferro

- Status changed from Unit: Impl to Unit: Rev

#13 - 24.07.2015 19:04 - Giuseppe Ferro

- Description updated

- Status changed from Unit: Rev to Closed

- Assignee deleted (Giuseppe Ferro)

Unit Review done: cb388cb