MARTe2-core - User story #1728

Add MARTe2 BareUS and FreeRTOS to jenkins

29.04.2024 17:06 - André Neto

Status:	Closed	**Start date:**	29.04.2024
Priority:	Normal	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0.00 hour
Target version:		**Spent time:**	0.00 hour
Git branch (link):		**SVN commit (link/?p=rev):**	
Git merge to develop (link):			

Description

DoD:

- MARTe2 job to compile MARTe2 BareUS and FreeRTOS

History

#1 - 26.06.2024 17:21 - Giuseppe Avon

- Assignee changed from Giuseppe Avon to André Neto

- Status changed from Code: Impl to Code: Rev

Added Ultrascale MainFPGA to Jenkins. The build uses the skip deploy option to avoid connecting to the JTAG. Validation policy is based on having all the four cores ELFs built, plus the FSBL. To suit the Jenkins needs and to have a cleaner ultrascale.sh script, the endless loop happening after a build failure was removed and converted to a sleep lasting up to 30 seconds waiting for all the elfs to be available.

Commit was done directly on develop 87e650c547f1264c8a376b19819f0c04989ad078

Things that should be improved:
- The export DISPLAY=dummy has no effect (tested on the same machine) and lot of errors are produced, complaining about the missing X or emulated framebuffer. Checked Xilinx and relevant Eclipse forums to find a suitable solution, most realistic implies installing a virtual framebuffer
- The virtual framebuffer also requires a Jenkins plugin which should be installed alongside.

#2 - 03.07.2024 17:19 - André Neto

- Assignee deleted (André Neto)

- Status changed from Code: Rev to Closed