RF PLC - User story #2551

Check code in PLC and compare it with gitlab code

08.04.2025 09:08 - Angela Salom

Status:	Closed	**Start date:**	08.04.2025
Priority:	Normal	**Due date:**	14.04.2025
Assignee:	Alfonso Ros	**% Done:**	100%
Category:		**Estimated time:**	4.00 hours
Target version:	Architecture definition V1.0	**Spent time:**	0.00 hour
Git branch (link):		**SVN commit (link/?p=rev):**	
Git merge to develop (link):			

Description

Related issues:

precedes User story #2552: Migration of present RF PLC code from step 7 class...	**New**	**15.04.2025**	**05.05.2025**

History

#1 - 08.04.2025 09:17 - Angela Salom

- Target version set to Architecture definition V1.0

- Assignee set to Alfonso Ros

#2 - 08.04.2025 09:18 - Angela Salom

- precedes User story #2552: Migration of present RF PLC code from step 7 classic to TIA Portal added

#3 - 08.04.2025 09:25 - Angela Salom

- % Done changed from 0 to 100

- Status changed from New to Closed

#4 - 08.04.2025 09:45 - Alfonso Ros

- File RFM-CS_2023-02-21.zip added

- File RFbackup.zip added

I connected to the PLC controller on Friday, April 4th, 2025, to download its internal program. Before this, I had already downloaded the PLC software available in the GitLab repository located at https://code.ifmif.org/lipac/topIFMIF_RF/-/tree/master/Resources/PLC_code?ref_type=heads.

After retrieving the internal program from the controller, I performed a comparison between the downloaded version and the one from the GitLab repository. The analysis confirmed that both programs are identical, with no differences found between them.

I attach both files.

The IP address assigned to the PLC is:

- PLC: 192.168.1.10/24

The PLC interface modules are connected via Profibus communication. There are three interface modules connected to the PLC, which acts as the Profibus master. The Profibus addresses assigned to each device are as follows:

- PLC (Master – Rack 1): Address 2
- Interface Module (Rack 2): Address 3
- Interface Module (Rack 3): Address 4
- Interface Module (Rack 4): Address 5

The identification of the RF_PLC hardware components can be found in the images attached at the top of this document.

Best regards,
Alfonso Ros

#5 - 08.04.2025 10:03 - Alfonso Ros

- File Communications Rf_PLC.png added

#6 - 08.04.2025 10:03 - Alfonso Ros

- File deleted (Communications Rf_PLC.png)

#7 - 08.04.2025 10:04 - Alfonso Ros

- File Communications_Rf_PLC.png added

#8 - 08.04.2025 10:15 - Alfonso Ros

- File deleted (Communications_Rf_PLC.png)

#9 - 08.04.2025 10:16 - Alfonso Ros

- File Hardware identification Rf_PLC_Rack4.PNG added

- File Hardware identification Rf_PLC_Rack3.PNG added

- File Hardware identification Rf_PLC_Rack2.PNG added

- File Hardware identification Rf_PLC_Rack1.PNG added

Files

RFbackup.zip	1.23 MB	08.04.2025	Alfonso Ros
RFM-CS_2023-02-21.zip	1.05 MB	08.04.2025	Alfonso Ros
Hardware identification Rf_PLC_Rack2.PNG	33.3 KB	08.04.2025	Alfonso Ros
Hardware identification Rf_PLC_Rack1.PNG	37.4 KB	08.04.2025	Alfonso Ros
Hardware identification Rf_PLC_Rack4.PNG	34.1 KB	08.04.2025	Alfonso Ros
Hardware identification Rf_PLC_Rack3.PNG	32.5 KB	08.04.2025	Alfonso Ros