

RF PLC - User story #2662

check if status of pre-driver can be added to PLC logic

02.06.2025 10:24 - Angela Salom

Status:	New	Start date:	02.06.2025
Priority:	Normal	Due date:	16.06.2025
Assignee:	Iago Dapena	% Done:	0%
Category:		Estimated time:	8.00 hours
Target version:	Architecture definition V1.0	Spent time:	0.00 hour
Git branch (link):		SVN commit (link/?p=rev):	
Git merge to develop (link):			
Description <ul style="list-style-type: none">- Ivan to send technical manual and define logic- Check if PLC can read the inputs.- include them into the architecture			