

Fast Logic Solver Test-bench - User story #814

Migrate UART to Labview

22.04.2021 19:14 - Katarina Cindric

Status:	Closed	Start date:	22.04.2021
Priority:	Normal	Due date:	
Assignee:	Pedro Lourenco	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:	Test-bench for PLC interface v0.1.0	Spent time:	0.00 hour
Git branch (link):		SVN commit (link/?p=rev):	
Git merge to develop (link):			
Description			
Migrate UART to Labview			

History

#1 - 05.05.2021 08:31 - Katarina Cindric

- Assignee deleted (Pedro Lourenco)

#2 - 11.05.2021 17:35 - Katarina Cindric

- Status changed from New to Code: Impl

#3 - 11.05.2021 17:36 - Katarina Cindric

- Status changed from Code: Impl to New

#4 - 11.05.2021 17:45 - Katarina Cindric

- Target version set to Test-bench for PLC interface v0.1.0

- Assignee set to Pedro Lourenco

#5 - 11.05.2021 17:46 - Katarina Cindric

- Status changed from New to Code: Impl

#6 - 02.06.2021 08:32 - Katarina Cindric

- Status changed from Code: Impl to Closed