

## Fast Logic Solver Test-bench - User story #817

### Simple C app to test UART

22.04.2021 19:15 - Katarina Cindric

<b>Status:</b>	Closed	<b>Start date:</b>	22.04.2021
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Katarina Cindric	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	Test-bench for PLC interface v0.1.0	<b>Spent time:</b>	0.00 hour
<b>Git branch (link):</b>		<b>SVN commit (link/?p=rev):</b>	
<b>Git merge to develop (link):</b>			
<b>Description</b>			
Simple C app to test UART			

#### History

**#1 - 05.05.2021 08:30 - Katarina Cindric**

- Assignee deleted (Katarina Cindric)

**#2 - 11.05.2021 17:35 - Katarina Cindric**

- Status changed from New to Code: Impl

**#3 - 11.05.2021 17:36 - Katarina Cindric**

- Status changed from Code: Impl to New

**#4 - 11.05.2021 17:46 - Katarina Cindric**

- Status changed from New to Code: Impl

**#5 - 11.05.2021 17:46 - Katarina Cindric**

- Target version set to Test-bench for PLC interface v0.1.0

- Assignee set to Pedro Lourenco

**#6 - 02.06.2021 08:32 - Katarina Cindric**

- Status changed from Code: Impl to Unit: Rev

**#7 - 15.06.2021 20:18 - Pedro Lourenco**

The simple C application based on the NIFpga C API was successfully used to test the UART firmware integrated into LabView FPGA firmware.

Application and firmware can be found in: [[

<https://vcis-gitlab.f4e.europa.eu/plourenco/flstestbench/-/tree/42694fba4bc84a3a823b0f58f65a51de38489125/Software/LabViewFpgaCApi/niUartLoopbackApp>]].

**#8 - 29.06.2021 17:43 - Katarina Cindric**

- Assignee changed from Pedro Lourenco to Katarina Cindric

**#9 - 29.06.2021 17:45 - Katarina Cindric**

- Status changed from Unit: Rev to Closed