

Fast Logic Solver Test-bench - User story #957

Develop FLS v0.7.0 test bench

05.10.2021 15:58 - Katarina Cindric

Status: Unit: Rev	Start date: 05.10.2021
Priority: Normal	Due date:
Assignee: Katarina Cindric	% Done: 0%
Category:	Estimated time: 0.00 hour
Target version: Test bench for FLS v0.7.0	Spent time: 0.00 hour
Git branch (link):	SVN commit (link/?p=rev):
Git merge to develop (link):	
Description Develop FLS v0.7.0 test bench	

History

#1 - 05.10.2021 15:59 - Katarina Cindric

- Status changed from New to Code: Impl

#2 - 05.10.2021 18:59 - Pedro Lourenco

FLSTestBench for FLS release fls_v0.7.0 successfully created with all basic testcases covered.

Additional minor modifications to register expected contents are foreseen but the test cases should not change.

Please find them here <https://vcis-gitlab.f4e.europa.eu/plourenco/flstestbench/-/commit/34185cf511d2c3508c774b3a33cfa7460f380c8f> .

#3 - 29.10.2021 15:54 - Katarina Cindric

- Status changed from Code: Impl to Unit: Rev

#4 - 29.10.2021 16:01 - Katarina Cindric

- Assignee changed from Pedro Lourenco to Katarina Cindric