

## Fast Logic Solver Test-bench - User story #958

### Develop FLS v0.7.1 test bench

05.10.2021 15:58 - Katarina Cindric

<b>Status:</b>	Unit: Rev	<b>Start date:</b>	05.10.2021
<b>Priority:</b>	Normal	<b>Due date:</b>	
<b>Assignee:</b>	Katarina Cindric	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>	Test bench for FLS v0.7.0	<b>Spent time:</b>	0.00 hour
<b>Git branch (link):</b>		<b>SVN commit (link/?p=rev):</b>	
<b>Git merge to develop (link):</b>			
<b>Description</b>			
Correct expected output debug behavior - the output to debug will not be related to the type of output, the same logic shall be applied to all the outputs			

### History

#### #1 - 05.10.2021 16:00 - Katarina Cindric

- Status changed from New to Code: Impl

#### #2 - 05.10.2021 19:05 - Pedro Lourenco

FLSTestBench for FLS release fls\_v0.7.1 successfully created with all basic testcases covered.

Additional minor modifications to register expected contents are foreseen but the test cases should not change.

Please find them here <https://vcis-gitlab.f4e.europa.eu/plourenco/flstestbench/-/commit/56ed6ef56c25b020a75797992922bdbcdf7e14b1>.

#### #3 - 29.10.2021 16:00 - Katarina Cindric

- Status changed from Code: Impl to Unit: Rev

#### #4 - 29.10.2021 16:00 - Katarina Cindric

- Assignee changed from Pedro Lourenco to Katarina Cindric