

Fast Logic Solver Test-bench - User story #974

Develop FC interface test bench v0.1.0

29.10.2021 16:05 - Katarina Cindric

Status: Unit: Rev	Start date: 29.10.2021
Priority: Normal	Due date:
Assignee: Pedro Lourenco	% Done: 0%
Category:	Estimated time: 0.00 hour
Target version: Test bench for FC interface v0.1.0	Spent time: 0.00 hour
Git branch (link):	SVN commit (link/?p=rev):
Git merge to develop (link):	
Description	
Develop FC interface test bench v0.1.0	

History

#1 - 29.10.2021 16:07 - Katarina Cindric

- Status changed from New to Arch: Rev

#2 - 12.11.2021 15:27 - Pedro Lourenco

FLSTestBench for FLS release fls_v0.9.x created with all the required (foreseen) methods for test routine automation.

Please find them here : [\[\[https://vcis-gitlab.f4e.europa.eu/plourenco/flstestbench/-/commit/5c8d5e1d9f087fb8675b6707dbc1b2d00aed1923\]\]](https://vcis-gitlab.f4e.europa.eu/plourenco/flstestbench/-/commit/5c8d5e1d9f087fb8675b6707dbc1b2d00aed1923)

#3 - 30.11.2021 09:27 - Katarina Cindric

- Status changed from Arch: Rev to Unit: Rev